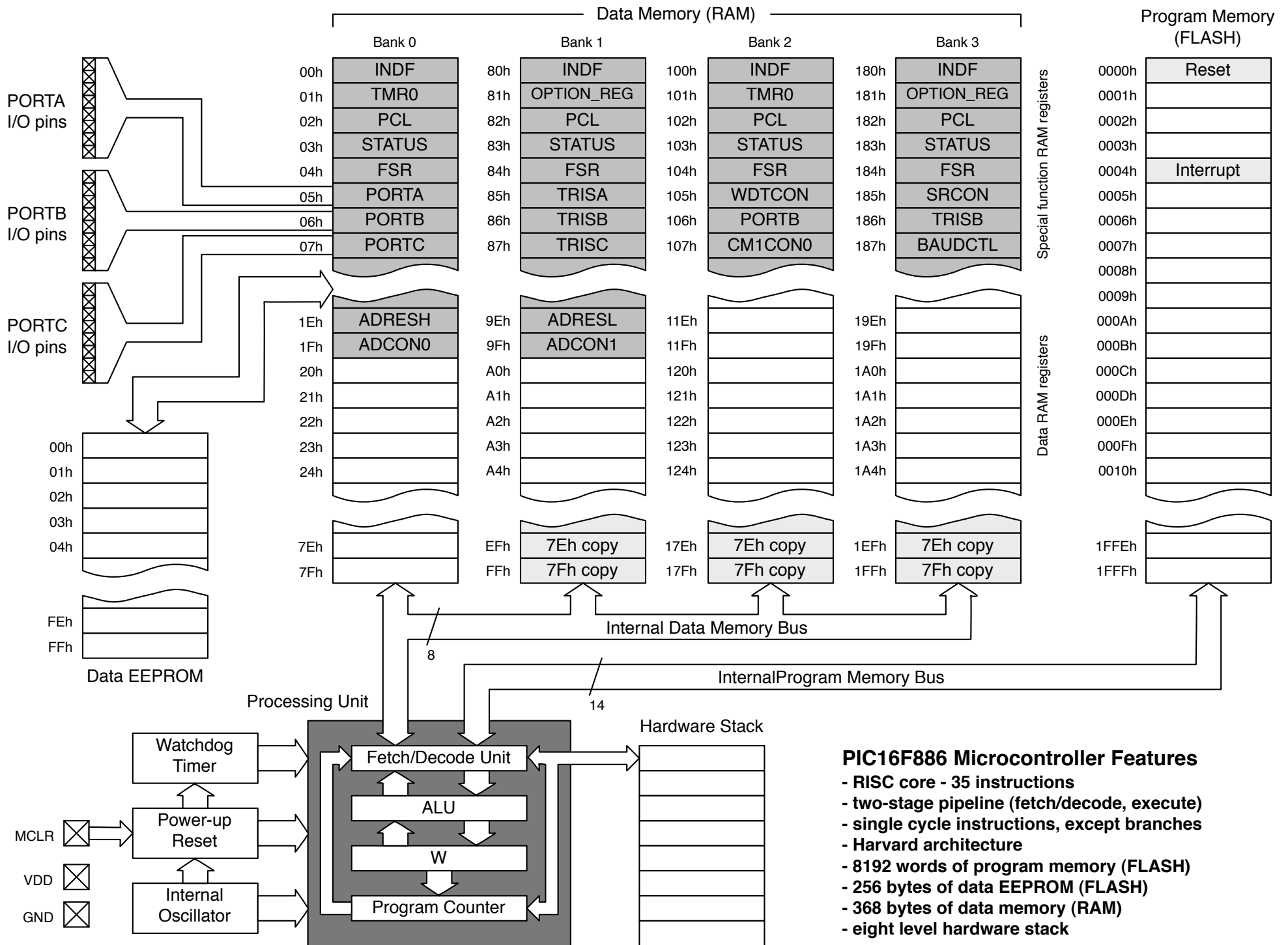


PIC16F886 Microcontroller



- PIC16F886 Microcontroller Features**
- RISC core - 35 instructions
 - two-stage pipeline (fetch/decode, execute)
 - single cycle instructions, except branches
 - Harvard architecture
 - 8192 words of program memory (FLASH)
 - 256 bytes of data EEPROM (FLASH)
 - 368 bytes of data memory (RAM)
 - eight level hardware stack